

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
DISNEY, D. ) Examiner: Unassigned  
Serial No.: Not Yet Unassigned ) Art Unit:  
Filing Date: August 26, 2003 )  
(Concurrently Herewith) )  
For: HIGH-VOLTAGE TRANSISTOR )  
WITH BURIED CONDUCTION LAYER )  
Divisional Application of: )  
Serial No. 10/392,622 )  
Filed: March 20, 2003 )  
Examiner: Tran, Long K. )  
Art Unit: 2818 )

**Preliminary Amendment**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-identified application, please amend the application as follows: